W-DC-9296  Previously named TU-W1340308
GaAs PHEMT MMIC Downconverter, 92 - 96GHz

Overview

W-DC-9296 is a MMIC trans-conductance (PHEMT) frequency downconverter. This MMIC is designed for RF frequencies in the range from 92GHz to 96GHz using LO signals within the 86GHz to 90GHz range. The circuit typically supplies low conversion loss for IF frequencies up to 6GHz and uses a notch filter to effectively remove any RF or LO signals at the IF port.

All bond pads and the die underside are gold plated. The MMIC is compatible with precision die attach methods, as well as thermo-compression and thermosonic wire bonding, making it ideal for MCM and hybrid microcircuit applications. All data shown herein is measured with the chip in a 50 Ohm environment and contacted with RF probes.

Features

- 92 – 96GHz RF frequency.
- <4 dB conversion loss.
- 10dB return loss.
- 40dB harmonic rejection.

Applications

- Narrow or wide bandwidth millimeter-wave imaging.
- High resolution radar.
- Sensing.
- P2P communications; short haul/high capacity/low interference links.
## Specification Overview

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>RF Frequency</td>
<td>92</td>
<td>96</td>
<td>GHz</td>
<td></td>
</tr>
<tr>
<td>LO Frequency</td>
<td>84</td>
<td>90</td>
<td>GHz</td>
<td></td>
</tr>
<tr>
<td>IF Frequency</td>
<td>2</td>
<td>5.4</td>
<td>8</td>
<td>GHz</td>
</tr>
<tr>
<td>Input Return Loss*</td>
<td>10</td>
<td></td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td>Output Return Loss*</td>
<td>10</td>
<td></td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td>Conversion Loss</td>
<td>1</td>
<td>2</td>
<td>11</td>
<td>dB</td>
</tr>
<tr>
<td>Drain Voltage (VDD)</td>
<td>0.7</td>
<td></td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Gate Voltage (VGG)</td>
<td>-1</td>
<td></td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Current*</td>
<td>11</td>
<td>15</td>
<td>mA</td>
<td></td>
</tr>
</tbody>
</table>

*Under large signal conditions.

## Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Rating</th>
</tr>
</thead>
<tbody>
<tr>
<td>Gate Voltage</td>
<td>-5V to 0.2V dc</td>
</tr>
<tr>
<td>Drain Voltage</td>
<td>5V</td>
</tr>
<tr>
<td>Drain Current</td>
<td>30mA</td>
</tr>
<tr>
<td>RF Input Power (all ports)</td>
<td>+18dBm</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>−65°C to +150°C</td>
</tr>
<tr>
<td>Channel Temperature</td>
<td>+150°C</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>−40°C to +85°C</td>
</tr>
</tbody>
</table>

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features proprietary protection circuitry, damage may occur on devices subjected to ESD. Proper ESD precautions should be taken to avoid performance degradation or loss of functionality.
Measured Performance Data

Test Conditions: IF = Fixed, 5.4GHz; RF Power = -10dBm; LO Power = 10dBm (@ (RF-IF) GHz)

Figure 1
TU-W1340308
Conversion Loss v RF Frequency

(Vdd=0.7V, Vgg=-1V, Idd=11-15mA)

Figure 2
TU-W1340308
2nd Harmonic Attenuation (from wanted signal)

(Vdd=0.7V, Vgg=-1V, Idd=11-15mA)
Measured Performance Data

Test Conditions: RF Power = -10dBm, 94GHz; LO Power = 10dBm (@ (RF-IF) GHz);

(Vdd=0.7V, Vgg=-1V, Idd=11-15mA)

Figure 3
TU-W1340308
Conversion Loss v IF Frequency

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Outline Drawing
## Pad Descriptions

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>LO</td>
<td>LO pad. This pad is DC coupled.</td>
</tr>
<tr>
<td>RF</td>
<td>RF pad. This pad is DC coupled.</td>
</tr>
<tr>
<td>IF</td>
<td>IF pad. This pad is DC coupled.</td>
</tr>
<tr>
<td>VDD</td>
<td>Drain Bias pad</td>
</tr>
<tr>
<td>VGG</td>
<td>Gate Bias pad.</td>
</tr>
<tr>
<td>BOTTOM</td>
<td>The die backside must be connected to RF/DC ground.</td>
</tr>
</tbody>
</table>

### Notes

1. All dimensions are in um.
2. Typical DC bond pads are 80um square.
3. RF bond pads are 60um square.
4. All pads have gold metalisation.
5. Gold backside metalisation.
6. Backside metal is ground.
7. Connections are not required for unlabelled bond pads.
8. Die thickness is 50um

### Die Packing Information

All die are delivered using gel-paks unless otherwise requested.
Die should be mounted on conductive material such as gold-plated metal to provide a good ground and suitable heat sink, if necessary.

1. Attaching the die using Au/Sn preforms is preferable. The Eutectic melt for Au/Sn occurs at approximately 280°C so the die (plus mount and preform) is initially heated up to 180°C and then it is heated for approximately 10 seconds to 280°C using a nitrogen heat gun. The device will survive 10 seconds at this temperature.

The static breakdown for GaAs devices is approximately 330°C.

2. Pure, dry nitrogen should be used as the heat source.

3. If the devices cannot be lifted/ placed by a vacuum device, then ESD die-lifting tweezers are preferable.

4. Supply lines should be decoupled with 100pF capacitors. Larger planar capacitors could be used if available.

5. Aluminium wire must not be used.
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